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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/707,546	12/20/2003	Munehiro Karasudani	22040-00025-US	1545
30678	7590 11/14/2005		EXAMINER	
CONNOLLY BOVE LODGE & HUTZ LLP			' NGUYEN, CUONG QUANG	
SUITE 800 1990 M STRE	ET NW		ART UNIT	PAPER NUMBER
WASHINGTON, DC 20036-3425			2811	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/707,546	KARASUDANI, MUNEHIRO				
Office Action Summary	Examiner	Art Unit				
	Cuong Q. Nguyen	2811				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on						
2a) ☐ This action is FINAL . 2b) ☑ This	on is FINAL . 2b)⊠ This action is non-final.					
, 	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
• 4)⊠ Claim(s) <u>1-4</u> is/are pending in the application.						
4a) Of the above claim(s) <u>1,2 and 4</u> is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>3</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9) The specification is objected to by the Examiner.						
10) The drawing(s) filed on is/are: a) accepted or b) dipected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:						
 Certified copies of the priority documents have been received. 						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)						
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail D	ate Patent Application (PTO-152)				
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date <u>12-22-03</u> .	6) Other:	ratent Application (FTO-132)				

DETAILED ACTION

Election/Restriction

Applicant's election with traverse of Embodiment 4 (Fig.5), claims 3 and 4 is 1. acknowledged. The traversal is on the ground(s) Fig.4 and Fig.4 illustrate the circuits in Fig.2 of Embodiment 1, so these figures should belong to Embodiment of Fig.2. Examiner agree with applicant. However, because applicant did not distinctly and specifically point out the supposed errors in the restriction requirement between elected Emodiment (Fig.5) and non-elected Embodiment (Fig.2 to Fig.4), the election has been treated as an election without traverse (MPEP § 818.03(a)).

The requirement is still deemed proper and is therefore made FINAL.

It is further noted that the limitation "an analog circuit with feedback loop; an analog signal line is wired outside the layout of said analog circuit." belong to non-elected Embodiment of Fig.6. So, claim 4 has been withdrawn from consideration.

Specification

Claim objected to because of the following informalities: the expression "wherein 2. said analog lines are wired outside the layout of said plurality of circuit block; or on the layout of a certain circuit block" should be change to "wherein said analog lines are wired on the layout of a certain circuit block" because limitation "analog lines are wired outside the layout of said plurality of circuit block" does not belong to elected Fig.5.

Appropriate correction is required. The title of the invention is not descriptive.

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3. A new title is required that is clearly indicative of the invention to which the claims are directed.

Claim Rejections - 35 USC § 112

4. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claim 3 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

The expression "analog control lines to another circuit block are wired" in claim 3 is unclear.

Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 4 is rejected under 35 U.S.C. 102(b) as being anticipated by Admitted prior art Fig.1.

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Admitted prior art Fig.1 discloses a semiconductor integrated circuit, comprising on the same chip: a plurality of circuit blocks composed by the CMOS process, which have ON/OFF functions of the power source; a control circuit (4) to control the ON/OFF functions of the power source of the plurality of circuit blocks; and analog control lines (105-1 to 105-3) connected between plurality of circuit blocks and the control circuit; wherein analog control lines are wired on the layout circuit block (3); when the power source of the certain circuit block (i.e. AM block) is turned ON by control circuit, the another circuit block (i.e. FM) is not controlled in a state of being ON simultaneously therewith. See also "BACKGROUND OF THE INVENTION".

Conclusion

- 6. Papers related to this application may be submitted to Technology center (TC) 2800 by facsimile transmission. Papers should be faxed to TC 2800 via the TC 2800 Fax center located in Crystal Plaza 4, room 4-C23. The faxing of such papers must conform with the notice published in the Official Gazette, 1096 OG 30 (November 15, 1989). The Group 2811 Fax Center number is (571) 273-8300.
- 7. Any inquiry concerning this communication or any earlier communication from the Examiner should be directed to CUONG Q NGUYEN whose telephone number is (571) 272-1661. The Examiner is in the Office generally between the hours of 6:30 AM to 5:00 PM (Eastern Standard Time) Monday through Thursday.
- 8. If attempts to reach the examiner by telephone are unsuccessful, the primary examiner Steven Loke who can be reached on (571) 272-1657.

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Cuong Nguyer

Primary examiner

11/2/05